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Yariv, A.;

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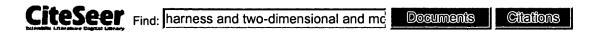
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MAP110-1012 Power Supply MAP110-1012 Power Supply - Title Dfm Description (Correct)

Dc Prototype Coil Driver Electronics Power Harness Drawn By Leandra Vicci Date Sheet Scale Dwg

15 [V]Each supply rated at 15[V]8[A]AC power harness mating with J1 uses Molex 09-50-3051-P shell

LM 675T LM 675T RH-10 RH-10 RH-10 Power Connector Power Switch DB15 DB-9 TITLE 3DFM

ftp.cs.unc.edu/pub/publications/techreports/01-031.pdf

A 20 Ampere Shunt Regulator For Controlling Individual.. - Martin Dobeck Jones (1995) (Correct)
The three boards are interconnected by a wiring harness and plug-in connectors. This facilitates by use of insulated supports and a special connector mounted on G10 laminate. Isolation for remote Connections to a module are via a multipin connector which is isolated from the adjacent module and www.aps.anl.gov/conferences/mirrored/www.cern.ch/accelconf/p95/ARTICLES/RPP/RPP06.PDF

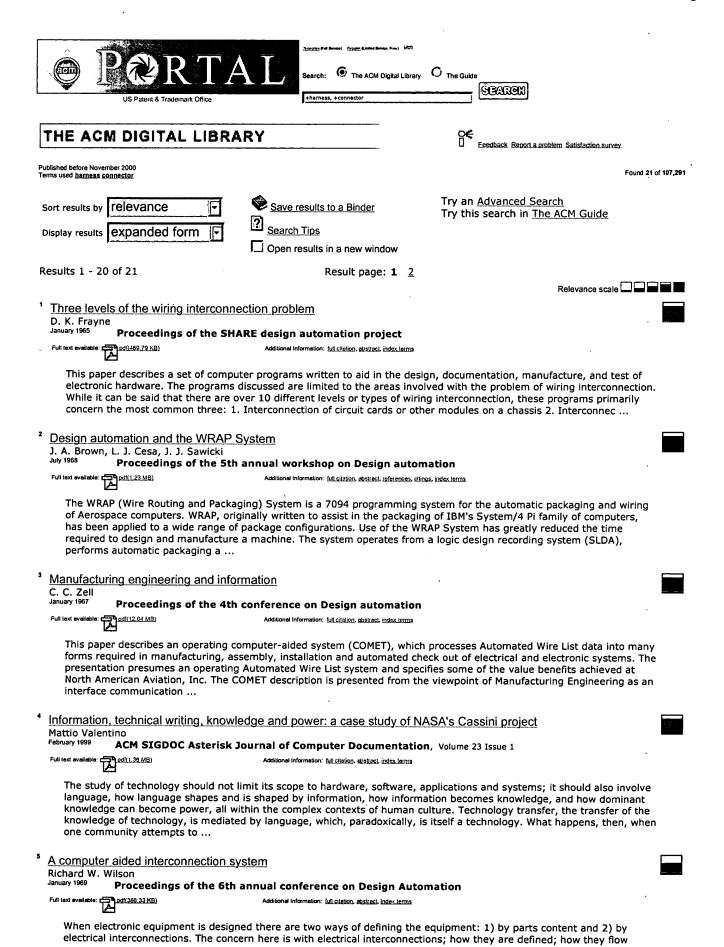
<u>SubjuGator: A Highly Maneuverable, Autonomous... - Laine, Nichols.. (2000) (Correct)</u> providers of valuable resources. In order to fully **harness** those resources, however, we must develop through the container pass through Burton **connectors** positioned on the side of the box. Bulkhead seal, and where appropriate, the holes for the **connectors** are tapped so that the **connectors** can be

www.me.ufl.edu/~dkn/papers/icra2000_sub.pdf

Computational Support for Concurrent Engineering of Cable...- Park, Lee, Cutkosky (1992) (Correct)
Support for Concurrent Engineering of Cable Harnesses Hisup Park Center for Design Research,
(e.g. connectivity, geometry) of the harnesses. The abstraction of design information results
Insulation Jacket Ferrule Nut Cap Copper Braid Connector Wire Transition Assembly Connector & Bundle cdr.stanford.edu/pub/CDR/Publications/Reports/FirstLink.ps

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through the phases of design; and how a computer-aided system has helped to create an efficient design tool out of a cumbersome manual data flow. By comparing the manual system (Fig 1) with the computer-aided system (F \dots

Enforcing architecture constraints Robert Balzer Joint proceedings of the second international software architecture workshop (ISAW-2) and international workshop on multiple perspectives in software development (Viewpoints '96) on SIGSOFT '96 workshops Additional Information: full citation, references, citings, index terms The design and performance of a pluggable protocols framework for real-time distributed object computing middleware Carlos O'Ryan, Fred Kuhns, Douglas C. Schmidt, Ossama Othman, Jeff Parsons IFIP/ACM International Conference on Distributed systems platforms Additional Information: full citation, obstract, references, citings To be an effective platform for performance-sensitive real-time and embedded applications, off-the-shelf CORBA middleware must preserve the communication-layer quality of service (QoS) properties of applications end-to-end. However, the standard CORBA GIOP/HOP interoperability protocols are not well suited for applications that cannot tolerate the message footprint size, latency, and jitter associated with general-purpose messaging and transport protocols. It is essential, therefore, to de ... Reuse of off-the-shelf components in C2-style architectures Nenad Medvidovic, Peyman Oreizy, Richard N. Taylor Proceedings of the 19th international conference on Software engineering Additional Information: full citation, references, citings, index terms Keywords: architectural styles, component-based development, graphical user interfaces (GUI), message-based architectures, software reuse Reuse of off-the-shelf components in C2-style architectures Nenad Medvidovic, Peyman Oreizy, Richard N. Taylor ACM SIGSOFT Software Engineering Notes , Proceedings of the 1997 symposium on Software reusability, Volume 22 Issue 3 Full text evailable: pdf(1.55 MB) Additional Information: full citation, references, citings, index terms Keywords: architectural styles, component-based development, graphical user interfaces, message-based architectures, software reuse Explaining issues in software reusability via an audio system analogy Robert Biddle, Ewan Tempero Proceedings of the 2nd Australasian conference on Computer science education pdf(813.16.KB) Additional Information: full citation, references, Index terms Compiler and tool support for debugging object protocols Sergey Butkevich, Marco Renedo, Gerald Baumgartner, Michal Young ACM SIGSOFT Software Engineering Notes , Proceedings of the 8th ACM SIGSOFT international symposium on Foundations of software engineering: twenty-first century applications, Volume 25 Issue Full text evailable: Red (1.06 MB) Additional Information: full citation, abstract, references, citings, index terms We describe an extension to the Java programming language that supports static conformance checking and dynamic debugging of object "protocols," i.e., sequencing constraints on the order in which methods may be called. Our Java protocols have a statically checkable subset embedded in richer descriptions that can be checked at run time. The statically checkable subtype conformance relation is based on Nierstrasz' proposal for regular (finite-state) process types, and is also very c ... Keywords: debugging, protocols, sequencing constraints

Multiple mass-market applications as components

David Coppit, Kevin J. Sullivan June 2000 Proceedings of the 22nd international conference on Software engineering	and the
Full text evailable: Additional information: full citation, abelizact, references, citings, index terms	
Truly successful models for component-based software development continue to prove elusive. One of the few is the of operating system, database and similar programs in many systems. We address three related problems in this price, we lack needed models. Second, we do not know the conditions under which such models can succeed. In particular, it is unclear whether the notable success with operating systems can be replicated. Third, we do not know whether certain specific models ca	paper.
Keywords: component-based software, package-oriented programming	
¹³ TouchCounters: designing interactive electronic labels for physical containers	
Paul Yarin, Hiroshi Ishii May 1999 Proceedings of the SIGCHI conference on Human factors in computing systems: the CHI is th	a limit
Full text evailable: Additional Information: full citation, sheltest, references, citings, index terms	
We present TouchCounters, an integrated system of electronic modules, physical storage containers, and shelving surfaces for the support of collaborative physical work. Through physical sensors and local displays, TouchCounters and display usage history information upon physical storage containers, thus allowing access to this information do the performance of real-world tasks. A distributed communications network allows this data to be exchanged with server, such that us	ıring
Keywords: distributed sensing, tangible interfaces, ubiquitous computing, visualization	
¹⁴ A proven operational CAD system for P.W.B. design-based on a mini-computer and featuring fully automatic placement and routing. G. L. Patterson, B. H. Phillips June 1976 Proceedings of the 13th conformer on Posicin Systems (Proceedings of the Proceedings (Proceedings of the Proceedings of the Proceedings of the Proceedings of the Proceedings (Proceedings of the Proceedings of the Proceedings of the Proceedings of the Proceedings (Proceedings of the Proceedings (Proceedings of the Proceedings of the Proceedings of the Proceedings of the Proceedings (Proceedings of the Proceedings of the Proceedings of the Proceedings of the Proceedings (Proceedings of the Proceedings of the Proceedings of the Proceedings of the Proceedings (Procedings of the Proceedings of th	- Cathallan
June 1976 Proceedings of the 13th conference on Design automation Full text available: 戶 adt(425.58.KB) Additional Information: [ull.citation, abstract, references, edings, index terms	
South and the control of the control	
The introduction of electronic processor controlled switching systems has led to a re-appraisal of the methods of deprinted wiring boards to cope with the sheer numbers of designs required and their increasing complexity. The Corpolicy is normally to realise the equipment on double sided printed wiring boards (pwb) containing a maximum of integrated circuits per board plus discrete components. (Figs. 1 and 2) PWB requirements are frequently changing techn	npany's 30
Using the Baby-Babble-Blanket for infants with motor problems: an empirical study H. J. Fell, H. Delta, R. Peterson, L. J. Ferrier, Z. Mooraj, M. Valleau October 1994 Proceedings of the first annual ACM conference on Assistive technologies	·
Full text evailable: Cdf(759.52 KB) Additional Information: full citation, abstract, references, ctings, index.terms	
Children with motor problems often develop to be passive, presumably because of an inability to communicate and control the environment. The Baby-Babble-Blanket (BBB), a pad with pressure switches linked to a Macintosh communicate and was developed to meet this need. Lying on the pad, infants use head-rolling, leg-lifting and kicking to produce digit sound. Data is collected by the BBB software on the infant's switch activations. An empirical study was carried out five-month-old infan	puter, tized
¹⁶ Interface to architecture: integrating technology into the environment in the Brain Opera	. \square
Maggie Orth August 1997 Proceedings of the conference on Designing interactive systems: processes, practices, metho	ds,
and techniques Full text evaluable: Col((1.41 MB) Additional information: full citation, references, index terms	
Keywords : architecture, collaboration, design, environment, furniture, interface, opera, physical interface, sensor theater	,
Memory: part III. Reasoning and inference: The commonsense algorithm as a basis for computer models of memory, inference, belief and contextual language comprehension Chuck Rieger June 1975 Proceedings of the 1975 workshop on Theoretical issues in patural language processing	human 🔀
Proceedings of the 1975 workshop on Theoretical Issues in Natural language processing	
Full lext evailable: Additional Information: <u>full citation</u> , abstract, references, citings Publisher, Site.	
The notion of a commonsense algorithm is presented as a basic data structure for modeling human cognition. This structure unifies many current ideas about human memory and information processing. The structure is defined by	

specifying a set of proposed cognitive primitive links which, when used to build up large structures of actions, states, statechanges and tendencies, provide an adequate formalism for expressing human plans and activities, as well as general mechanisms and computer algor ...

Session II - empirical studies: Narratives at work: story telling as cooperative diagnostic activity Julian E. Orr December 1986 Proceedings of the 1986 ACM conference on Computer-supported cooperative work odi(896.15.KB) The diagnostic process for copiers involves narration of the process, including a description of the state of the machine. This follows from the fact that copiers are elaborate assemblages of relatively simple mechanisms, and the problem in diagnosis is not so much the testing of components as keeping track of the tests and making sense of their results. The anecdotal re-telling of this narrative to one's associates constitutes the mechanism for incorporating the diagnostic experience into the c ... Expert systems in law: out of the research laboratory and into the marketplace R. E. Susskind December 1987 Proceedings of the first international conference on Artificial intelligence and law Full text evailable: pdf(1.01 MB) Additional Information: full citation, abstract, citings, index terms The major goal of workers in the field of expert systems in law can be summarized as follows: through the use of computer technology, to make scarcs, human legal knowledge and expertise more widely available and easily accessible. This paper is directly concerned with that goal. It is divided into two parts. The first discusses a three year interdisciplinary research project in expert systems in law that involved the Law Faculty and the Programming Research Group of the University of Oxford ... ²⁰ A design analysis of a hybrid technology multithreaded architecture for petaflops scale computation3 Thomas Sterling, Larry Bergman Proceedings of the 13th international conference on Supercomputing pdf(1.92,MB) Additional Information: full citation, references, citings, index terms Results 1 - 20 of 21 Result page: 1 2

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